

WEST**Help** **Logout****Main Menu** | **Search Form** | **Posting Counts** | **Show S Numbers** | **Edit S Numbers****Search Results -**

Term	Documents
MC68302.USPT.	61
MC68302S	0
INTERFACE.USPT.	243183
INTERFACES.USPT.	63072
(MC68302 SAME INTERFACE).USPT.	36

Database: US Patents Full-Text Database**Refine Search:**

mc68302 same interface

Search History

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT	mc68302 same interface	36	<u>L3</u>
USPT	mc68302 and interface	57	<u>L2</u>
USPT	interface same (gate array) same Asic	94	<u>L1</u>

WEST**Searches for User *athompson* (Count = 664)****Queries 615 through 664.**[Latest](#) | [Prev](#) | [Next](#) | [Oldest](#)[Edit](#)[Help](#)[Return](#)[Main Menu](#)[Logout](#)

S #	Comment Database	Query
S664	USPT	((interface\$ buffer\$) and fpga) and mux
S663	USPT	(interface\$ buffer\$) and fpga
S662	USPT	(interface\$ buffer\$) same fpga
S661	USPT	interface\$ buffer\$
S660	USPT	((mask programmable and rout\$) and interface) and field programmable
S659	USPT	(mask programmable and rout\$) and interface
S658	USPT	mask programmable and rout\$
S657	USPT	5128831.pn. or 4953060.pn.
S656	USPT	mc68302 same interface
S655	USPT	mc68302 and interface
S654	USPT	interface same (gate array) same Asic
S653	USPT	5680332.pn. or 5537580.pn. or 5513122.pn. or 5469367.pn. or 5140526.pn.
S652	USPT	5920711.pn.
S651	USPT	5058026.pn. or 5980084.pn.
S650	USPT	((automated assembly) and disassembly) and search and collision
S649	USPT	((automated assembly) and disassembly) and search
S648	USPT	((automated assembly) and disassembly) and search strategy
S647	USPT	((automated assembly) and disassembly) and collision detection
S646	USPT	(automated assembly) and disassembly
S645	USPT	automated assembly
S644	USPT	face same module generation
S643	USPT	face same design
S642	USPT	face design
S641	USPT	((fpga same map\$) and placement) and modula\$
S640	USPT	(fpga same map\$) and placement
S639	USPT	fpga same map\$

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT	l9 and mux\$	4	<u>L11</u>
USPT	l8 and mux\$	12	<u>L10</u>
USPT	l7 and fpga and asic	24	<u>L9</u>
USPT	l7 and gate array and asic	36	<u>L8</u>
USPT	iob	417	<u>L7</u>
USPT	l5 and asic	59	<u>L6</u>
USPT	l1 and interconnect	306	<u>L5</u>
USPT	l3 and mux	37	<u>L4</u>
USPT	l1 and fpga	44	<u>L3</u>
USPT	l1 same fpga	1	<u>L2</u>
USPT	interface\$ buffer\$	1034	<u>L1</u>